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22850 7590 08/09/2005

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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/716,556	11/20/2003	Tadashi Iguchi	245469US2DIV	5687

TITLE OF INVENTION: METHOD OF MANUFACTURING A NONVOLATILE SEMICONDUCTOR MEMORY DEVICE HAVING A STACKED GATE STRUCTURE
09/28/2005 MBEYENE2 00000094 10716556

01 FC:1501	1400.00 OP				
02 FC:1504	300.00 OP				
APPLN. TYPE	SMALL ENTITY				
nonprovisional	NO	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
		\$1400	\$300	\$1700	11/09/2005

EXAMINER	ART UNIT	CLASS-SUBCLASS
CHEN, JACK S J	2813	438-257000

1. Change of correspondence address or indication of "Fee Address" (37 CFR 1.363).
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2. For printing on the patent front page, list
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1 OBLON, SPIVAK,
2 McCLELLAND, MAIER
3 & NEUSTADT, P.C.

3. ASSIGNEE NAME AND RESIDENCE DATA TO BE PRINTED ON THE PATENT (print or type)

PLEASE NOTE: Unless an assignee is identified below, no assignee data will appear on the patent. If an assignee is identified below, the document has been filed for recordation as set forth in 37 CFR 3.11. Completion of this form is NOT a substitute for filing an assignment.

(A) NAME OF ASSIGNEE

KABUSHIKI KAISHA TOSHIBA

(B) RESIDENCE: (CITY and STATE OR COUNTRY)

Kawasaki-shi, JAPAN

Please check the appropriate assignee category or categories (will not be printed on the patent): Individual Corporation or other private group entity Government

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a. Applicant claims SMALL ENTITY status. See 37 CFR 1.27. b. Applicant is no longer claiming SMALL ENTITY status. See 37 CFR 1.27(g)(2).

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Authorized Signature

Joseph Scafetta Jr.

Date

SEP 26 2005

Typed or printed name

Joseph Scafetta, Jr.

Registration No.

Reg. No. 26,803

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